



WORKSHOP ON

ELECTRONIC CIRCUIT DESIGN-2017

October 26 - November 1, 2017

Organized by



IE Student Chapter

*Department of Applied Electronics &
Instrumentation Engineering*

Guru Nanak Institute of Technology, Kolkata



Venue

Guru Nanak Institute of Technology

Committee

Chief Patron : Mr. Taranjit Singh, MD, JIS Group
Patrons : Prof. S.S. Dutta Gupta, Director, JIS Group
: Prof. (Dr.) Annapurna Das, Director, GNIT
: Prof (Dr.) Shantanu Sen, Principal, GNIT
: Mr. Uday Shankar Mukherjee, Dy. Director, JIS Group
Chairperson : Mrs. Bapita Roy, H.O.D.- AEIE

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Prof. Anil Kr. Ghoshal, Prof., AEIE
Dr. Adhish Kr. Chakraborty, Registrar, GNIT

Organizing Committee

Mrs. Jayita Datta, Asst.Prof., AEIE
Mrs. Paramita Banerjee ,Asst.Prof.,AEIE
Mrs. Suparna Maity, Asst.Prof.,AEIE
Mrs.Sanghamitra Layek , Asst.Prof.,AEIE
Mr. Mrinmoy Dutta,JTA,AEIE
Mrs. Saswati Chatterjee, JTA,AEIE
Mrs. Nabamita Paul, JTA,AEIE

Technical committee:

Mrs.Sanghamitra Layek , Asst.Prof.,AEIE
Mrs. Suparna Maity, Asst.Prof.,AEIE
Mr. Sudip Kuila, Asst.Prof.,AEIE
Mr. Mrinmoy Dutta,JTA,AEIE
Mrs. Saswati Chatterjee, JTA,AEIE
Mrs. Nabamita Paul, JTA,AEIE

Coordinators:

Miss. Uma Ghosh,Asst.Prof.,AEIE
Mrs. Santana Das,Asst.Prof.,AEIE

Address for Correspondence

Workshop on “ Electronic Circuit Design-2017”
Department of Applied Electronics &
Instrumentation Engineering
Guru Nanak Institute Of Technology
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Kolkata-700114

Contact Persons

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Electronic circuit design is a key element of the whole electronics industry. Even though much is achieved using software, the design of the electronic circuits themselves is still key both in terms of analogue circuits and digital circuits. So it is versatile for any project work to be executed. Proper designing of the circuit results a successful implementation of electronic circuit based projects.

After designing the fabrication of the same is to be done by proper technique. In this workshop the minor project work will be allotted to the participants. They will be taught to design the hardware circuit such that proper circuit components can be chosen for a particular circuit.

Hands on experiments will encourage the students to execute the hardware project with a true knowledge of design and fabrication of electronic circuits.

Discourse and Practical experiments will focus on the following topics:

- Basics of design of Electronic circuits
- Fabrication of the circuits
- Cost estimation of a project.
- Basics of soldering and de-soldering.

- Basic knowledge of circuit elements.
- Application of the circuit design procedure in real life project mainly hardware project.

Registration Fee:
Students (UG) Rs.60 /-

Each Registered Participant will be provided with detailed study materials (Soft copy/hard copy) on design and fabrication of electronic circuit. Tea in between sessions will be arranged. Certificates will be awarded to successful participants during valedictory session.

Given registration form should be filled up by the intending participants and same will be submitted to the coordinator by 26th October '2017, along with the registration fees. Number of participants will be limited to 50 only due to constraint in laboratory. So first come first serve system will be followed.

Time Schedule:

Date	2p.m-2:30p.m	2:30p.m.-4:00p.m.	4:00p.m.-4:15p.m.	4:15p.m.-5:30p.m.
26/10/17	Inauguration	Technical Session	Tea break	Technical Session
27/10/17	Technical Session	Technical Session	Tea break	Technical Session
30/10/17	Technical Session	Technical Session	Tea break	Technical Session
31/10/17	Technical Session	Technical Session	Tea break	Technical Session
01/11/17	Technical Session	Technical Session	Tea break	Technical Session

Registration Form

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Department of Applied Electronics & Instrumentation Engineering

Guru Nanak Institute of Technology

Name

Date of Birth

Sex

Qualification

Designation

Organization

Address for Communication

Mobile No

E-Mail ID

Declaration

I hereby declare that the information furnished above is true to the best of my knowledge.

Date:

Place:

